

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David G. Figueroa et al.

Title: ARRANGEMENTS FOR MINIMIZING SIGNAL PATH DISCONTINUITIES

Docket No.: 884.B23US1

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Examiner: Sara W. Crane

Group Art Unit: 2811

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

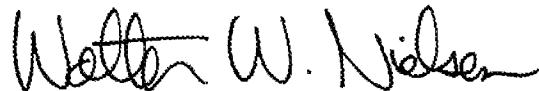
X Revocation and Power of Attorney (2 pgs.) with attached copy of executed Assignment from inventors to Intel Corporation (4 pgs.).

**If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.**

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